18F Series Microcontroller

STARTUP	
IN	CONNECT (0x80)
OUT	FIRMWARE VERSION
OUT	USER BAUDRATE (H)
OUT	USER BAUDRATE (L)
OUT	DEVICE SERIES
OUT	DEVICE ID (H)
OUT	DEVICE ID (L)
OUT	LOADER ROM (U)
OUT	LOADER ROM (H)
OUT	LOADER ROM (L)
OUT	BLOCK ERASE (H)
OUT	BLOCK ERASE (L)
OUT	BLOCK WRITE
OUT	ACK (0x80)
IN	LOADER_GO (0x81)

EEPROM READ	
IN	0x01
IN	ADDR (H)
IN	ADDR (L)
IN	0
OUT	DATA
OUT	ACK (0x80)

EEPROM WRITE	
IN	0x02
IN	ADDR (H)
IN	ADDR (L)
IN	DATA
IN	CRC
OUT	ACK (0x80)

ROM ERASE	
IN	0x04
IN	ADDR (U)
IN	ADDR (H)
IN	ADDR (L)
OUT	ACK (0x80)

ROM READ	
IN	0x08
IN	ADDR (U)
IN	ADDR (H)
IN	ADDR (L)
OUT	DATA
OUT	ACK (0x80)

ROM WRITE	
IN	0x10
IN	ADDR (U)
IN	ADDR (H)
IN	ADDR (L)
IN	DATA [0..n]
IN	CRC
OUT	ACK (0x80)

SET BAUDRATE	
IN	0x20

CONSTANTS	
CONNECT	0x80
LOADER_GO	0x81
ACK	0x80
ERR_CRC	0x8A
ERR_VERIFY	0x8B

16F Series Microcontroller

STARTUP	
IN	CONNECT (0x80)
OUT	FIRMWARE VERSION
OUT	USER BAUDRATE (H)
OUT	USER BAUDRATE (L)
OUT	DEVICE SERIES
OUT	DEVICE ID (H)
OUT	DEVICE ID (L)
OUT	LOADER ROM (U)
OUT	LOADER ROM (H)
OUT	LOADER ROM (L)
OUT	BLOCK ERASE (H)
OUT	BLOCK ERASE (L)
OUT	BLOCK WRITE
OUT	ACK (0x80)
IN	LOADER_GO (0x81)

EEPROM READ	
IN	0x01
IN	ADDR (H)
IN	ADDR (L)
IN	0
OUT	DATA
OUT	ACK (0x80)

EEPROM WRITE	
IN	0x02
IN	ADDR (H)
IN	ADDR (L)
IN	DATA
IN	CRC
OUT	ACK (0x80)

ROM ERASE	
IN	0x04
IN	ADDR (H)
IN	ADDR (L)
IN	Dummy Byte
OUT	ACK (0x80)

ROM READ	
IN	0x08
IN	ADDR (H)
IN	ADDR (L)
IN	Dummy Byte
OUT	DATA (H)
OUT	DATA (L)
OUT	ACK (0x80)

ROM WRITE	
IN	0x10
IN	ADDR (H)
IN	ADDR (L)
IN	Dummy Byte
IN	DATA [0..n]
IN	CRC
OUT	ACK (0x80)

SET BAUDRATE	
IN	0x20

CONSTANTS	
CONNECT	0x80
LOADER_GO	0x81
ACK	0x80
ERR_CRC	0x8A
ERR_VERIFY	0x8B